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Amendments to the Specification

Please replace the title at page 1, line 1, with the following amended title:

FREQUENCY MULTIPLIER WITH COMBINED PHASE COMPARATOR AND CHARGE PUMP CIRCUIT

Please replace the paragraph at page 1, lines 2 through 6 with the following amended paragraph:

RELATED APPLICATION(S)

This application is a continuation of Application No. 09/414,761, filed October 7, 1999, now U.S. Patent No. 6,275,072.

The entire teachings of the above application are incorporated herein by reference.

Please delete the paragraphs at page 3, line 25, which starts with "In accordance with the present invention" through page 5, lines 7-15, which starts with "Preferably, the first branch".

Please replace the paragraph at page 5, lines 16 through 22 with the following amended paragraph:

The A phase comparator may be utilized in a frequency multiplier circuit. In that implementation, the phase comparator is combined with a frequency generating circuit such as a voltage controlled oscillator which generates an output signal at a frequency that is a multiple of an input frequency. The phase comparator provides a phase comparison of an edge of the input signal and an edge of the output signal and controls the frequency generating circuit based on the comparison. Preferably, a divider divides the frequency of the output signal to provide the a window signal.